

Applicant: Vincent Dupaquis et al. PATENT APPLICATION

Serial No.: 10/615,475 Group Art Unit: 2124

Filed: July 7, 2003

For: EFFICIENT MULTIPLICATION SEQUENCE FOR LARGE

INTEGER OPERANDS WIDER THAN THE MULTIPLIER HARDWARE

Supplemental Information Disclosure Statement with Certification under 37 CFR § 1.97(e)(1)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicant's duty of disclosure under 37 CFR § 1.97(e). A copy of each of the cited references is enclosed.

FOREIGN PATENT DOCUMENT

Publication No.	<u>Published Date</u>	Country	
0 161 089	November 13, 1985	EPO	
0 206 763	December 30, 1986	EPO	

The undersigned hereby certifies that the items of information contained in this Supplemental Information Disclosure Statement were cited in a communication received from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signed: Deput State Commissioner State Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date: March 24, 2004

Respectfully submitted,

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LIST OF PRIOR ART CITED BY APPLICANT			Applicants: Vincent Dupaquis et al.							
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^{*}Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.